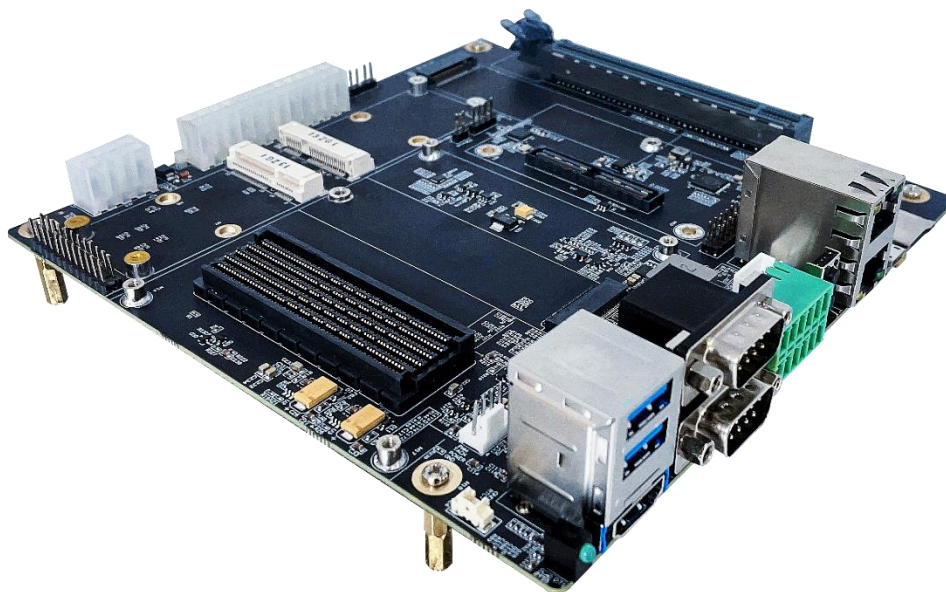


Y-C8 Carrier Board Datasheet



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Document History

Version	Date	Description of Change
V1.0	March 25, 2022	Initial Release
V1.1	June 27, 2022	Changed the Document Name: Specification -> Datasheet



Electronic components and circuits are very sensitive to electrostatic discharge, although the company will do anti-static protection design on the main interface of the board when designing circuit board products, but it is difficult to do anti-static safety protection for all components and circuits. Therefore, it is recommended to follow esd safety precautions when handling any circuit board component. Esd protection measures include but are not limited to the following:

- During transportation or storage, place the card in an ESD bag and do not take it out until installation.
- Release the static electricity before touching the board. Wear a discharge grounding wrist strap.
- Operate the circuit board only in electrostatic discharge safety area.
- Avoid moving circuit boards in carpeted areas.
- Avoid direct contact with electronic components on the board by edge contact.

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Chapter 1. Introduction

The Y-C8 is a standard Mini-ITX carrier board with NVIDIA® Jetson™ AGX ORIN, AGX Xavier series core modules. Suitable for compact deployment requirements. The main ports is designed for electrostatic safety protection, and the high reliability power supply application scheme is adopted. The input power supply has the function of overvoltage and reverse polarity protection, and has abundant external port. The whole board adopts the wide temperature model. In order to facilitate the shell structure design, the important interface design of Y-C8 board is led out on one side.

The Y-C8 board can be equipped with hundreds of functional modules through two miniPCIe connectors (including USB2.0 and PCIe X1 signals) to further expand system functions. You can expand out Four full speed USB3.0 signals, four GIGAB it network signals, two full speed SATA signals, and a maximum of 256GB Mini PCIe storage, 4G communication modules, various formats of video acquisition/output cards, AD acquisition cards, multiple serial ports, voice acquisition/output cards, and multi-function IO cards.....

1.1 Product Specifications

- Input Power : DC +12V
- Temperature Range : -40°C to 85°C
- Size : 170mm × 170mm ×34mm
- Weight : 260g

1.2 Order Information

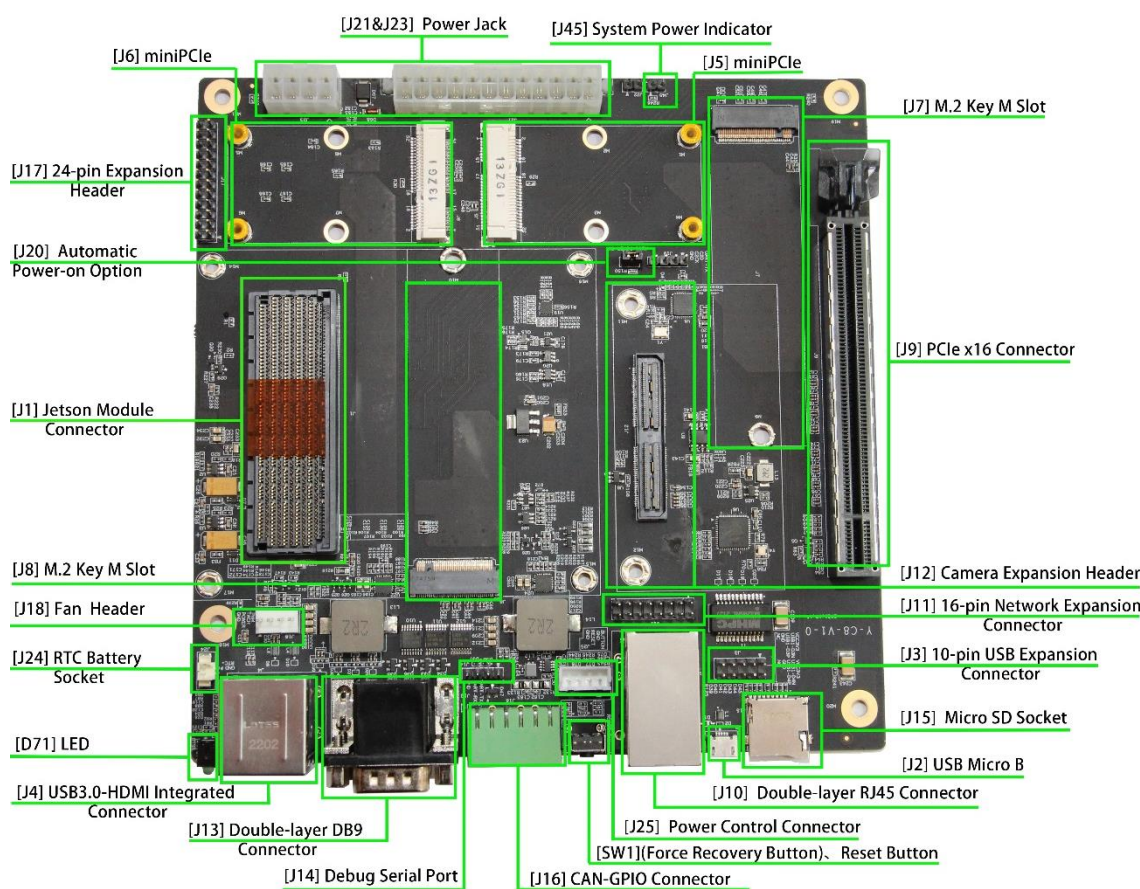
Model	Function
Y-C8	NVIDIA® Jetson™ AGX ORIN, AGX Xavier series core module interface expansion board.

Taobao Store Address : <https://shop333807435.taobao.com/>

Jingdong Store Address : <https://mall.jd.com/index-11467104.html?from=pc>

Ali International Station address : <https://plink-ai.en.alibaba.com/>

Chapter 2. Interface Function Description



2.1 LED Indicators

LED	Description
D71	SOC Regulator Power LED (Green)
J45	External power indicator of the cabinet enclosure

2.2 Buttons

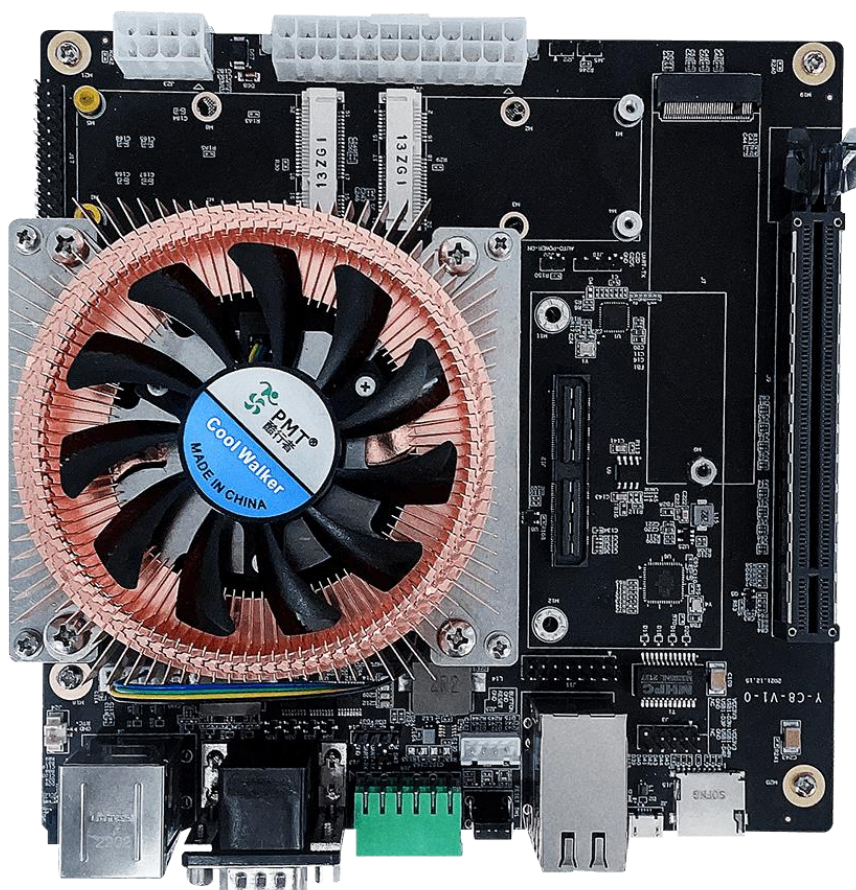
Button	Description	Usage
SW1	Recovery button	Used to enter Force Recovery Mode. Button is held down while either system is first powered on, or by pressing and releasing reset button while recovery button is pressed.
SW2	Reset button	Used to force a full system reset.

2.3 Functional Connector

Connector	Description
J1	Jetson AGX Xavier/AGX ORIN Module Connector (65×11)
J2	Micro USB B Connector, supporting Device mode only (including USB Recovery)
J3	10-pin USB Expansion Connector (USB 2.0)
J4	USB 3.0 Type A (×2 Stacked) & HDMI Type A Stacked Connector
J5&J6	miniPCIe Connector (52-pin)
J7&J8	M.2 Key M Slot (75-pin)
J9	PCIe x16 Connector
J10	RJ45 Ethernet Socket, RA , Female
J11	16-pin Ethernet Expansion Connector (Upper RJ45 port)
J12	Camera Expansion Header (120-pin)
J13	DB9 Connector, Male (×2 Stacked)
J14	Debug Serial Port
J15	Micro SD Socket
J16	GPIO & CAN Connector
J17	24-pin Expansion Header (2×12 ,2.00mm pitch)
J18	Fan-Header (4-pin)
J20	AUTO-POWER-ON
J21&J23	Power Jack
J24	RTC Battery Socket
J25	Power Control Connector
J45	System Power Indicator

Chapter 3. Installation and Use

3.1 Installation effect drawing



3.2 Method of use

- (1) Ensure that all external systems are powered off.
- (2) Install the Jetson core module onto the J1 high-speed connector, using even force with good alignment between the connectors. Install the core module fixing screws after the module is installed in place.
- (3) Install necessary external cables. (For example: display cable connected to HDMI display, power input cable for system power, USB cable connecting keyboard and mouse, camera, MiniPCIe function expansion module...)
- (4) Connect the power cable to the power supply.
- (5) Y-C8 You can set this parameter to automatic power-on by default or switch on to enable the system to work.
- (6) For the system without a protective cover, do not move the hardware system after the system is powered on. Do not touch the circuit board or any electronic components directly with your body.

3.3 Recovery mode

Jetson core module can work in normal mode and Recovery mode, in which file system update, kernel update, Boot Loader update, BCT update and other operations can be performed.

To enter the Recovery mode, perform the following steps:

- (1) Turn off the system power supply.
- (2) Use a micro-USB cable to connect y-C8's Micro-USB port (J2) with Jetson's development host USB port.
- (3) Hold down the RECOVERY button (SW1) and power the system. Hold down the SW2 button for more than 3 seconds and release the RECOVERY button.
- (4) The system enters Recovery mode, and you can perform subsequent operations.

Chapter 4. Interface Definition Description

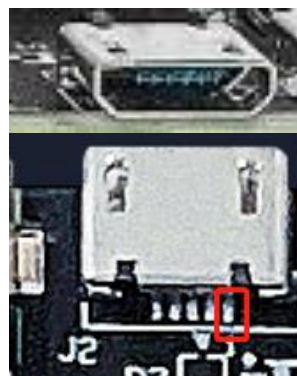
4.1 Jetson Module Connector [J1]

Function	Connect to the NVIDIA Jetson series AGX Orin / AGX Xavier module
Silk-screen	J1
Model	699-pin SO-DIMM
Pin Descriptions	For the pin definition of this connector, see the pin definition instructions in the NVIDIA Jetson Series AGX ORIN/AGX Xavier Core Module Data Book.



4.2 USB Micro B [J2]

Function	Supporting Device mode only (including USB Recovery)			
Silk-screen	J2			
Model	Type-b standard Micro USB 2.0 port			
Pin Descriptions	Pin		Signal	
	1	VBUS	2	USB0_N
	3	USB0_P	4	NC
	5	GND		
	Position of pin 1: in the red box on the right side of the picture.			



4.3 USB Expansion Connector [J3]

Function	USB2.0 signal expansion connector
Silk-screen	J3
Model	10pin(2×5, 2.0mm pitch)

Pin Descriptions

Pin	Signal	Pin	Signal
1	VCC_3V3	2	VCC_3V3
3	USB1_D3_N	4	USB1_D4_N
5	USB1_D3_P	6	USB1_D4_P
7	GND	8	GND
9	NC	10	NC

Position of pin 1: in the red box on the right side of the picture.



4.4 USB & HDMI Stacked Connector[J4]

Function HDMI Type A & USB3.0 Connector

Silk-screen J4

Model USB 3.0 Type A (×2 Stacked) & HDMI Type A Stacked Connector

Pin Descriptions USB3.0 pin definition:

Pin	Signal	Pin	Signal
1	VBUS	2	USB_N
3	USB_P	4	GND
5	UPHY_RX_N	6	UPHY_RX_P
7	GND	8	UPHY_TX_N
9	UPHY_TX_P		

HDMI pin definition:

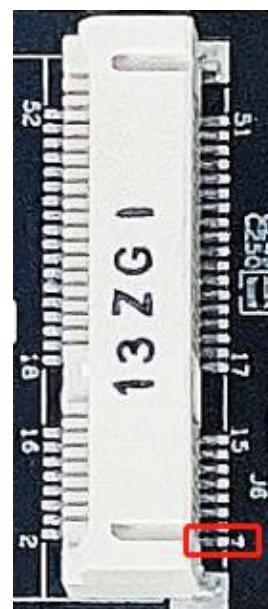
Pin	Signal	Pin	Signal
1	TXD2_P	2	TXD2_SHIELD
3	TXD2_N	4	TXD1_P
5	TXD1_SHIELD	6	TXD1_N
7	TXD0_P	8	TXD0_SHIELD
9	TXD0_N	10	TXC_P
11	TXC_SHIELD	12	TXC_N
13	HDMI_CEC	14	RESERVED
15	SCL	16	SDA
17	DDC/CEC_GND	18	VCC_5V
19	HP_DET		

When equipped with the Jetson AGX Xavier module, the lower USB has only 2.0 signal.



4.5 miniPCIe Connector [J5&J6]

Function	miniPCIe Connector			
Silk-screen	J5 & J6			
Model	5.6mm high supports full-length and half-length miniPCIe connectors for expansion cards			
Pin Descriptions	Pin	Signal	Pin	Signal
	1	WAKE	2	3.3V
	3	NC	4	GND
	5	NC	6	1.5V
	7	PEIC1_CLKREQ	8	UIM_PWR
	9	GND	10	UIM_DATA
	11	PEIC1_REFCLK_N	12	UIM_CLK
	13	PEIC1_REFCLK_P	14	UIM_RESET
	15	GND	16	UIM_VPP
	17	NC	18	GND
	19	NC	20	WI_DISABLE
	21	PEIC_CARD_SEL	22	PEIC1_REST
	23	PERN	24	3.3V
	25	PERP	26	GND
	27	GND	28	1.5V
	29	GND	30	SMB_CLK
	31	PETN	32	SMB_DAT
	33	PETP	34	GND
	35	GND	36	CON_USB2_D_N
	37	GND	38	CON_USB2_D_P
	39	VCC_3V3_PCIE	40	GND
	41	VCC_3V3_PCIE	42	NC
	43	PEIC_SEL	44	NC
	45	NC	46	NC
	47	NC	48	1.5V
	49	NC	50	GND
	51	NC	52	3.3V
	Position of pin 1: in the red box on the right side of the picture. When the Jetson AGX Xavier module is installed, the PCIe signal at the J6 connector position is unavailable.			



4.6 M.2 Key M Slot [J7&J8]

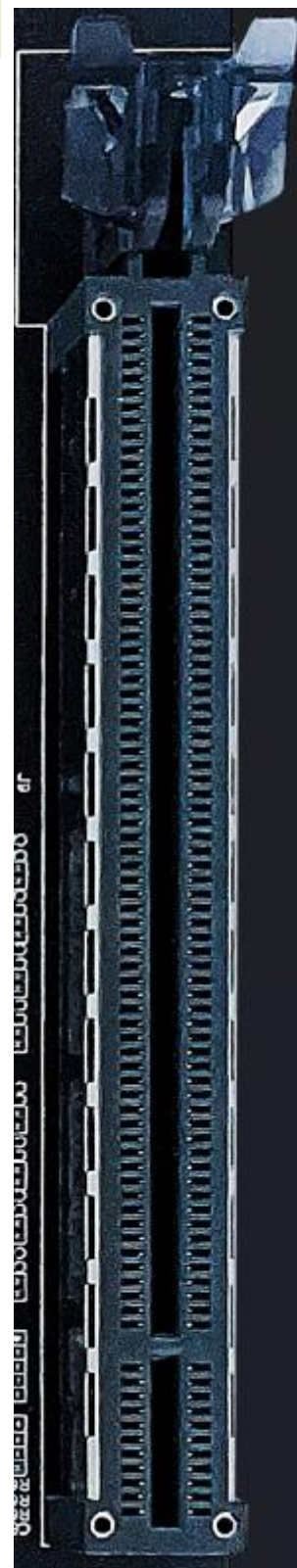
Function	M.2 Key M Slot			
Silk-screen	J7 & J8			
Model	M.2 Key M Slot (75-pin)			
Pin Descriptions	Pin	Signal	Pin	Signal
	1	GND	2	3.3V
	3	GND	4	3.3V
	5	UPHY_RX5_N	6	NC
	7	UPHY_RX5_P	8	NC
	9	GND	10	LED
	11	UPHY_RX5_N	12	3.3V
	13	UPHY_RX5_P	14	3.3V
	15	GND	16	3.3V
	17	UPHY_RX4_N	18	3.3V
	19	UPHY_RX4_P	20	NC
	21	GND	22	NC
	23	UPHY_RX4_N	24	NC
	25	UPHY_RX4_P	26	NC
	27	GND	28	NC
	29	UPHY_RX3_N	30	NC
	31	UPHY_RX3_P	32	NC
	33	GND	34	NC
	35	UPHY_RX3_N	36	NC
	37	UPHY_RX3_P	38	NC
	39	GND	40	SMB_CLK
	41	UPHY_RX2_N	42	SMB_DATA
	43	UPHY_RX2_P	44	ALERT
	45	GND	46	NC
	47	UPHY_RX2_N	48	NC
	49	UPHY_RX2_P	50	PERST#
	51	GND	52	CLKREQ#
	53	UPHY_REFCLK0_N	54	PEWAKE#
	55	UPHY_REFCLK0_P	56	NC
	57	GND	58	NC
	59	NC	60	NC
	61	NC	62	NC
	63	NC	64	NC
	65	NC	66	NC
	67	NC	68	SUSCLK(32KHz)
	69	NC	70	3.3V
	71	GND	72	3.3V
	73	GND	74	3.3V
	75	GND		

M.2 Key M port is not available at J8 using the Jetson AGX Xavier module.



4.7 PCIe Expansion Connector [J9]

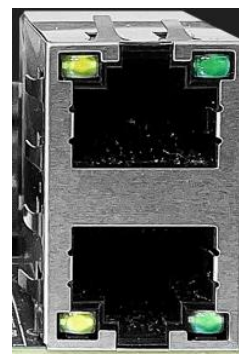
Function	PCIe Expansion Connector			
Silk-screen	J9			
Model	PCIe x16 Slot			
Pin Descriptions	Pin	Signal	Pin	Signal
	A1	GND	B1	12V
	A2	12V	B2	12V
	A3	12V	B3	12V
	A4	GND	B4	GND
	A5	SPI2_SCK	B5	I2C_GPO_CLK
	A6	SPI2_MISO	B6	I2C_GPO_DAT
	A7	SPI2_MOSI	B7	GND
	A8	SPI2_CS0	B8	3.3V
	A9	3.3V	B9	SPI3_MISI
	A10	3.3V	B10	3.3V
	A11	PEX_L5_RST_N	B11	SPI3_MOSI
	A12	GND	B12	PEX_L5_CLKPEQ_N
	A13	PEX_CLK5_P	B13	GND
	A14	PEX_CLK5_N	B14	NVHS0_TX0_P
	A15	GND	B15	NVHS0_TX0_N
	A16	NVHS0_SLVS_RX0_P	B16	GND
	A17	NVHS0_SLVS_RX0_N	B17	PRSNT2
	A18	GND	B18	GND
	A19	NC	B19	NVHS0_TX1_P
	A20	GND	B20	NVHS0_TX1_N
	A21	NVHS0_SLVS_RX0_P	B21	GND
	A22	NVHS0_SLVS_RX0_N	B22	GND
	A23	GND	B23	NVHS0_TX2_P
	A24	GND	B24	NVHS0_TX2_N
	A25	NVHS0_SLVS_RX2_P	B25	GND
	A26	NVHS0_SLVS_RX2_N	B26	GND
	A27	GND	B27	NVHS0_TX3_P
	A28	GND	B28	NVHS0_TX3_N
	A29	NVHS0_SLVS_RX3_P	B29	GND
	A30	NVHS0_SLVS_RX3_N	B30	PRSNT2
	A31	GND	B31	NC
	A32	NC	B32	GND
	A33	NC	B33	NVHS0_TX4_P



A34	GND	B34	NVHS0_TX4_N
A35	NVHS0_SLVS_RX4_P	B35	GND
A36	NVHS0_SLVS_RX4_N	B36	GND
A37	GND	B37	NVHS0_TX5_P
A38	GND	B38	NVHS0_TX5_N
A39	NVHS0_SLVS_RX5_P	B39	GND
A40	NVHS0_SLVS_RX5_N	B40	GND
A41	GND	B41	NVHS0_TX6_P
A42	GND	B42	NVHS0_TX6_N
A43	NVHS0_SLVS_RX6_P	B43	GND
A44	NVHS0_SLVS_RX6_N	B44	GND
A45	GND	B45	NVHS0_TX7_P
A46	GND	B46	NVHS0_TX7_N
A47	NVHS0_SLVS_RX7_P	B47	GND
A48	NVHS0_SLVS_RX7_N	B48	GND
A49	GND	B49	GND
Only PCIE X8 signals are supported			

4.8 RJ45 Ethernet Connector [J10]

Function	RJ45 Ethernet Connector			
Silk-screen	J10			
Model	RJ45 Ethernet Socket, RA , Female			
Pin Descriptions	Pin		Signal	
	1	TP0+	2	TP0-
	3	TP1+	4	TP2+
	5	TP2-	6	TP1-
	7	TP3+	8	TP3-
	For an upper-layer RJ45 network port, you need to extend a single-port nic through the miniPCIE port and connect the extended NIC to the J11 pin.			



4.9 Ethernet Expansion Connector [J11]

Function	Ethernet Expansion Connector
Silk-screen	J11
Model	16pin(2×8, 2.0mm pitch)

Pin Descriptions

Pin	Signal	Pin	Signal
1	GND	2	3.3V
3	3.3V	4	3.3V
5	ETH_ACT	6	ETH_LINK
7	NC	8	NC
9	ET0-	10	ET0+
11	ET1-	12	ET1+
13	ET2-	14	ET2+
15	ET3-	16	ET3+

Position of pin 1: in the red box on the right side of the picture.

Use the miniPCIe port to expand the single-port NIC, which corresponds to the upper-layer RJ45 double-layer network port.



4.10 Camera Expansion Header [J12]

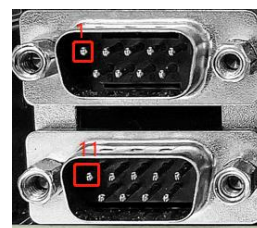
Function	Camera Expansion Connector			
Silk-screen	J12			
Model	0.5mm pitch, 120pin qsh-060-01-h-d-a-k-tr connector			
Pin Descriptions	Pin	Signal	Pin	Signal
	1	CSI0_D0_P	2	CSI1_D0_P
	3	CSI0_D0_N	4	CSI1_D0_N
	5	GND	6	GND
	7	CSI0_CLK_P	8	CSI1_CLK_P
	9	CSI0_CLK_N	10	CSI1_CLK_N
	11	GND	12	GND
	13	CSI0_D1_P	14	CSI1_D1_P
	15	CSI0_D1_N	16	CSI1_D1_N
	17	GND	18	GND
	19	CSI2_D0_P	20	CSI3_D0_P
	21	CSI2_D0_N	22	CSI3_D0_N
	23	GND	24	GND
	25	CSI2_CLK_P	26	CSI3_CLK_P
	27	CSI2_CLK_N	28	CSI3_CLK_N
	29	GND	30	GND
	31	CSI2_D1_P	32	CSI3_D1_P
	33	CSI2_D1_N	34	CSI3_D1_N
	35	GND	36	GND
	37	CSI4_D0_P	38	CSI6_D0_P
	39	CSI4_D0_N	40	CSI6_D0_N



	41	GND	42	GND
	43	CSI4_CLK_P	44	CSI6_CLK_P
	45	CSI4_CLK_N	46	CSI6_CLK_N
	47	GND	48	GND
	49	CSI4_D1_P	50	CSI6_D1_P
	51	CSI4_D1_N	52	CSI6_D1_N
	53	GND	54	GND
	55	T2	56	T3
	57	T4	58	T5
	59	CSI5_D0_P	60	CSI7_D0_P
	61	CSI5_D0_N	62	CSI7_D0_N
	63	GND	64	GND
	65	CSI5_CLK_P	66	CSI7_CLK_P
	67	CSI5_CLK_N	68	CSI7_CLK_N
	69	GND	70	GND
	71	CSI5_D1_P	72	CSI7_D1_P
	73	CSI5_D1_N	74	CSI7_D1_N
	75	I2C3_CLK_1V8	76	NC
	77	I2C3_DAT_1V8	78	NC
	79	GND	80	GND
	81	2.8V	82	2.8V
	83	2.8V	84	T6
	85	T7	86	NC
	87	I2C2_CLK_1V8	88	CAM1_MCLK03_1V8
	89	I2C2_DAT_1V8	90	CAM1_PWDN_1V8
	91	CAM0_MCLK02_1V8	92	CAM1_RST_1V8
	93	CAM0_PWDN_1V8	94	CAM2_MCLK04_1V8
	95	CAM0_RST_BUF_1V8	96	NC
	97	NC	98	NC
	99	GND	100	GND
	101	T8	102	1.8V
	103	NC	104	NC
	105	I2C4_CLK_1V8	106	NC
	107	I2C4_DAT_1V8	108	3.3V
	109	T9	110	3.3V
	111	NC	112	NC
	113	NC	114	NC
	115	GND	116	GND
	117	T10	118	3.3V
	119	VDD_SYS_EN_1V8	120	3.3V

4.11 DB9 Connector [J13]

Function	DB9 Connector			
Silk-screen	J13			
Model	DB9 Connector, Male (×2 Stacked)			
Pin Descriptions	Pin	Signal	Pin	Signal
	1	NC	2	UART2_RX_RS232
	3	UART2_TX_RS232	4	NC
	5	GND	6	NC
	7	NC	8	NC
	9	NC	10	
	11	NC	12	UART1_RX_RS232
	13	UART1_TX_RS232	14	NC
	15	GND	16	NC
	17	NC	18	NC
	19	NC	20	
	When loading AGX XAVIER module in the system, /dev/ttyTHS1 for the upper-layer DB9 connector and /dev/ttyTHS0 for the lower-layer DB9 connector. Position of pin 1: in the red box on the right side of the picture.			



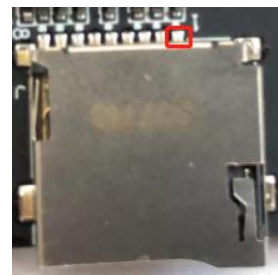
4.12 Debug Serial Port [J14]

Function	Debug Serial Port			
Silk-screen	J14			
Model	4pin (2.0mm pitch)			
Pin Descriptions	Pin	Signal	Pin	Signal
	1	3V3	2	UART3_TX_3V3
	3	UART3_RX_3V3	4	GND
	When the AGX Xavier module is installed, the debugging serial port corresponds to the device name /dev/ttyTHS4. Position of pin 1: in the red box on the right side of the picture.			



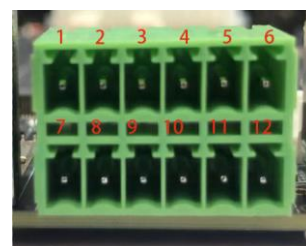
4.13 Micro SD Socket [J15]

Function	Micro SD Socket			
Silk-screen	J15			
Model	Micro SD (TF) Socket			
Pin Descriptions	Pin	Signal	Pin	Signal
	1	DATA2	2	DATA3
	3	CMD	4	3V3
	5	CLK	6	GND
	7	DATA0	8	DATA1
	9	CD		
	Position of pin 1: in the red box on the right side of the picture.			



4.14 GPIO & CAN Connector[J16]

Function	GPIO & CAN Connector			
Silk-screen	J16			
Model	2EDGKRH-3.5-2X6			
Pin Descriptions	Pin	Signal	Pin	Signal
	1	CAN1_H	2	3V3
	3	CAN1_L	4	GND
	5	GND	6	GPIO08
	7	CAN0_H	8	GPIO09
	9	CAN0_L	10	GPIO17
	11	GND	12	GPIO27(PWM)
The pin sequence of this interface signal is shown in the figure on the right. When equipped with AGX Xavier module: The mapping number of GPIO08 in the system is: 256 The mapping number of GPIO09 in the system is: 257 The mapping number of GPIO17 in the system is: 417 The mapping number of GPIO27 in the system is: 393				



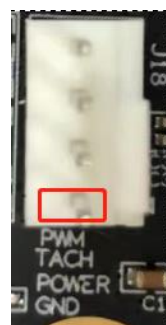
4.15 24-pin Expansion Header [J17]

Function	24pin Expansion Header			
Silk-screen	J17			
Model	24pin (2×12 ,2.00mm pitch)			
Pin Descriptions	Pin	Signal	Pin	Signal
	1	5V	2	3.3V
	3	GND	4	GND
	5	SPI1_MOSI_3V3	6	I2S2_CLK_3V3
	7	SPI1_MISO_3V3	8	I2S2_DOUT_3V3
	9	SPI1_SCK_3V3	10	I2S2_DIN_3V3
	11	SPI1_CS0_N_3V3	12	I2S2_FS_3V3
	13	SPI1_CS1_N_3V3	14	GND
	15	GND	16	I2C5_CLK_3V3
	17	PWM01_3V3	18	I2C5_DAT_3V3
	19	GND	20	GND
	21	GPIO35_PWM3_3V3	22	MCLK05_3V3
	23	GND	24	GND
	Position of pin 1: in the red box on the right side of the picture.			



4.16 Fan-Header [J18]

Function	Fan-Header			
Silk-screen	J18			
Model	4pin			
Pin Descriptions	Pin	Signal	Pin	Signal
	1	GND	2	POWER
	3	TACH	4	PWM
	Position of pin 1: in the red box on the right side of the picture.			



4.17 AUTO-POWER-ON [J20]

Function	AUTO-POWER-ON			
Silk-screen	J20			
Model	2pin (2.00mm pitch)			
Pin Descriptions	Pin	Signal	Pin	Signal
	1	3V3	2	ACOK



Position of pin 1: in the red box on the right side of the picture.

Short-circuit this interface, the system will automatically power on by default, if not short-circuit, you need to connect the button through J25, and start it through the button.

4.18 Power Jack [J21 & J23]

Function	24pin or 8pin power jack			
Silk-screen	J21 & J23			
Model	Power Jack			
Pin Descriptions	The J21 power input port is defined as follows:			
	Pin		Signal	
	1	+3.3V	2	+3.3V
	3	GND	4	+5V
	5	GND	6	+5V
	7	GND	8	PWR_OK
	9	+5V	10	+12V
	11	+12V	12	+3.3V
	13	+3.3V	14	-12V
	15	GND	16	PS_ON#
	17	GND	18	GND
	19	GND	20	NC
	21	+5V	22	+5V
	23	+5V	24	GND
	The J23 power input port is defined as follows:			
Pin		Signal		
1	GND	2	GND	
3	GND	4	GND	
5	VCC	6	VCC	
7	VCC	8	VCC	
When in use, you only need to supply power to one of the interfaces.				
The pin sequence is as shown on the right.				



4.19 RTC Battery Socket [J24]

Function	RTC Battery Socket			
Silk-screen	J24			
Model	2pin (2.00mm pitch)			
Pin Descriptions	Pin	Signal	Pin	Signal
	1	VCC_3V3	2	GND
Position of pin 1: in the red box on the right side of the picture.				



4.20 Power Control Connector [J25]

Function	Power Control Connector			
Silk-screen	J25			
Model	XH2.54-4P Female			
Pin Descriptions	Pin	Signal	Pin	Signal
	1	BUTTON	2	GND
	3	RESET	4	GND
Position of pin 1: in the red box on the right side of the picture.				
Connect this interface to the switch button of the chassis, and cancel the short-circuit of J20, then the system can be set to button start.				



4.21 System Power Indicator [J45]

Function	System Power Indicator			
Silk-screen	J45			
Model	2pin (2.00mm pitch)			
Pin Descriptions	Pin	Signal	Pin	Signal
	1	LED+	2	LED-
Position of pin 1: in the red box on the right side of the picture.				

